MOSFETs Silicon N-channel MOS (U-MOSIV)

TK70J04K3Z

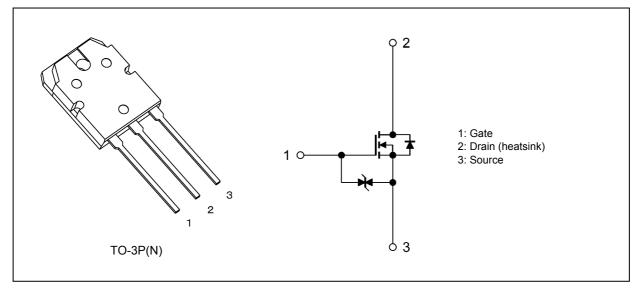
1. Applications

- Switching Voltage Regulators
- DC-DC Converters
- Motor Drivers

2. Features

- (1) Low drain-source on-resistance: $R_{DS(ON)} = 3.2 \text{ m}\Omega$ (typ.) (V_{GS} = 10 V)
- (2) Low leakage current: $I_{DSS} = 10 \ \mu A \ (max) \ (V_{DS} = 40 \ V)$
- (3) Enhancement mode: V_{th} = 3.0 to 4.0 V (V_{DS} = 10 V, I_{D} = 1 mA)

3. Packaging and Internal Circuit



4. Absolute Maximum Ratings (Note) (Ta = 25°C unless otherwise specified)

Characteristics			Symbol	Rating	Unit
Drain-source voltage			V _{DSS}	40	V
Gate-source voltage			V _{GSS}	±20	
Drain current (DC)		(Note 1)	Ι _D	70	A
Drain current (pulsed)		(Note 1)	I _{DP}	280	
Power dissipation	(T _c = 25°C)		PD	125	W
Single-pulse avalanche energy		(Note 2)	E _{AS}	184	mJ
Avalanche current			I _{AR}	70	Α
Channel temperature		(Note 3)	T _{ch}	175	°C
Storage temperature		(Note 3)	T _{stg}	-55 to 175	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Start of commercial production 2009-10 2014-01-17 Rev.2.0

5. Thermal Characteristics

Characteristics	Symbol	Max	Unit
Channel-to-case thermal resistance	R _{th(ch-c)}	1.2	°C/W
Channel-to-ambient thermal resistance	R _{th(ch-a)}	50	

Note 1: Ensure that the channel temperature does not exceed 175°C.

Note 2: V_DD = 25 V, T_ch = 25°C (initial), L = 39 $\mu H,\,R_G$ = 25 $\Omega,\,I_{AR}$ = 70 A

Note 3: The definitions of the absolute maximum channel and storage temperatures are based on AEC-Q101.

Note: This transistor is sensitive to electrostatic discharge and should be handled with care.

6. Electrical Characteristics

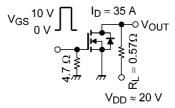
6.1. Static Characteristics (T_a = 25°C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current	I _{GSS}	V_{GS} = ±16 V, V_{DS} = 0 V	_	_	±10	μA
Drain cut-off current	I _{DSS}	V _{DS} = 40 V, V _{GS} = 0 V	-	—	10	
Drain-source breakdown voltage	V _{(BR)DSS}	I _D = 10 mA, V _{GS} = 0 V	40	_	_	V
Drain-source breakdown voltage	V _{(BR)DSX}	I _D = 10 mA, V _{GS} = -20 V	20	—	—	
Gate threshold voltage	V _{th}	V _{DS} = 10 V, I _D = 1 mA	3.0	_	4.0	
Drain-source on-resistance (Note 4)	R _{DS(ON)}	V _{GS} = 10 V, I _D = 35 A		3.2	4.1	mΩ

Note 4: Measured at lead standoff.

6.2. Dynamic Characteristics ($T_a = 25^{\circ}C$ unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Input capacitance	C _{iss}	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz		4500	_	pF
Reverse transfer capacitance	C _{rss}]	_	800	_	
Output capacitance	C _{oss}]		1150	_	
Switching time (rise time)	tr	See Figure 6.2.1.		20	_	ns
Switching time (turn-on time)	t _{on}]		45	_	
Switching time (fall time)	t _f]		30	_	
Switching time (turn-off time)	t _{off}]		95	_	



Duty $\leq 1\%$, t_w = 10 μ s

Fig. 6.2.1 Switching Time Test Circuit

6.3. Gate Charge Characteristics ($T_a = 25^{\circ}C$ unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Total gate charge (gate-source plus gate-drain)	Qg	$V_{DD} \approx 32 \text{ V}, \text{ V}_{GS} = 10 \text{ V}, \text{ I}_{D} = 70 \text{ A}$	_	100	—	nC
Gate-source charge	Q _{gs}		_	45	_	
Gate-drain charge	Q _{gd}		_	55	_	

6.4. Source-Drain Characteristics ($T_a = 25^{\circ}C$ unless otherwise specified)

Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Reverse drain current (DC)	(Note 5)	I _{DR}	—			70	А
Reverse drain current (pulsed)	(Note 5)	I _{DRP}	—	_	_	280	
Diode forward voltage		V _{DSF}	I _{DR} = 70 A, V _{GS} = 0 V	_	_	-1.2	V
Reverse recovery time		t _{rr}	I _{DR} = 70 A, V _{GS} = 0 V		48		ns
Reverse recovery charge		Q _{rr}	-dI _{DR} /dt = 50 A/μs	_	26	_	nC

Note 5: Ensure that the channel temperature does not exceed 175°C.

TK70J04K3Z

7. Marking (Note)

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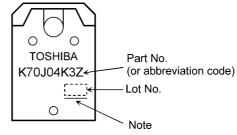
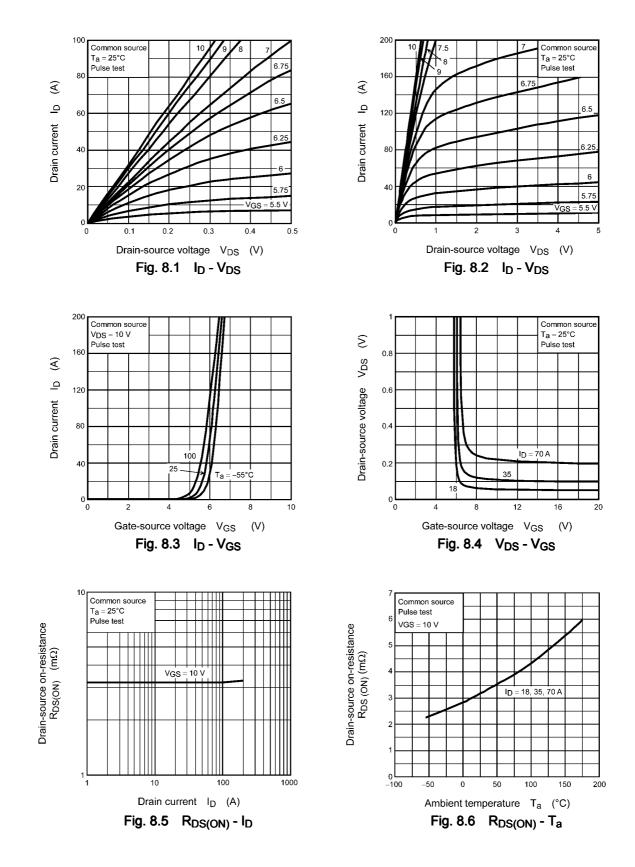


Fig. 7.1 Marking

Note: A line under a Lot No. identifies the indication of product Labels. Not underlined: [[Pb]]/INCLUDES > MCV Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]] Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product. The RoHS is the Directive 2011/65/EU of the European Parliament and of the Council of 8 June 2011 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.

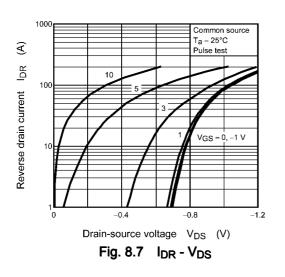
8. Characteristics Curves (Note)



S

Gate threshold voltage

Common source $V_{DS} = 10 V$ $I_{D} = 1 mA$ Pulse test



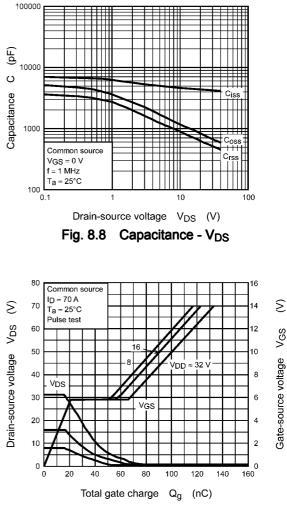
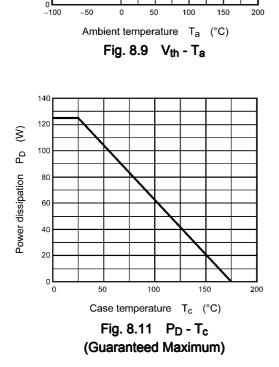
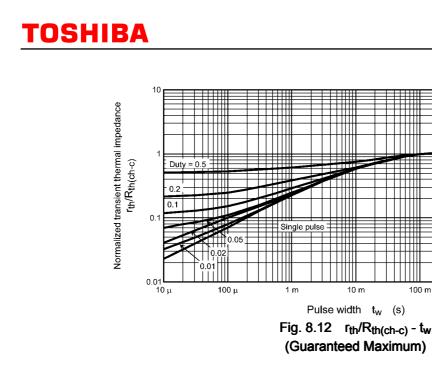
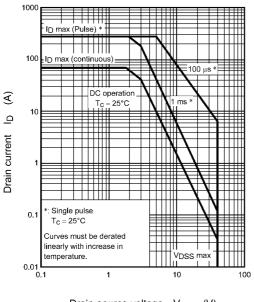


Fig. 8.10 Dynamic Input/Output Characteristics







Drain-source voltage V_{DS} (V) Fig. 8.13 Safe Operating Area (Guaranteed Maximum)

200 L = 39 μH VDD = 25 V (ſш) 160 Avalanche energy EAS 120 80 4(0L 25 75 125 50 100 150 175 200 Channel temperature (initial) T_{ch} (°C)

111

100 m

10 m

PDM

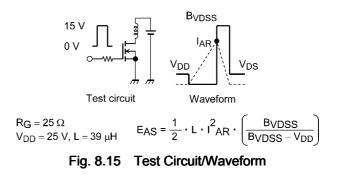
Duty = t/T

Rth(ch-c) = 1.2 °C/W

10

1

Fig. 8.14 EAS - Tch (Guaranteed Maximum)

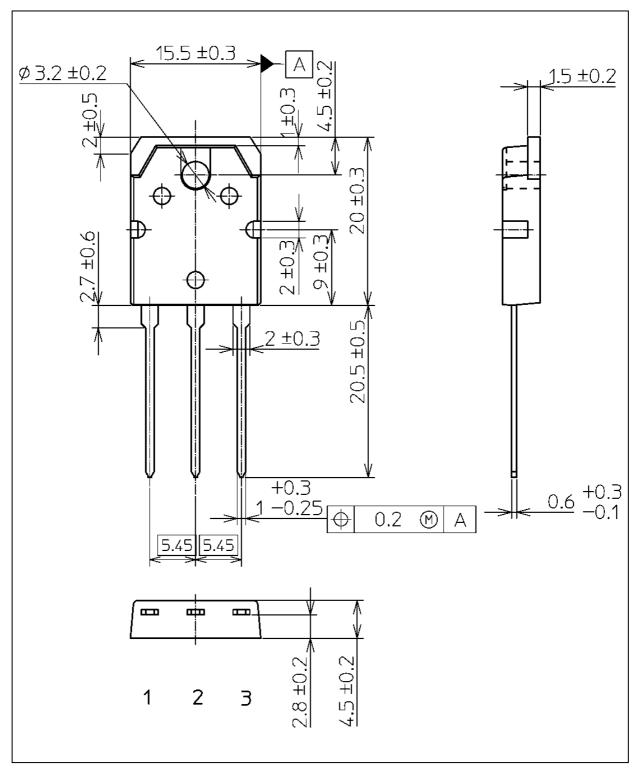


Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

Package Dimensions

TK70J04K3Z

Unit: mm



Weight: 4.6 g (typ.)

Package Name(s)	
JEITA: SC-65	
TOSHIBA: 2-16C1S	
Nickname: TO-3P(N)	

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